THREE DAYS FACULTY DEVELOPMENT PROGRAMME ON

Embedded Systems with Machine Learning

10th -12th April 2019

> ### Registration Form

Name   : ....

Gender   : Male / Female

Qualification : ....

Designation : ....

Department   : ....

Institution   : ....

Address for Communication:

……………………………………………………..

……………………………………………………..

………………………….. PIN: ....

E-Mail   : ....

Phone (Off.) : ....

Mobile No.   : ....

DD No. & Date: ....

Name of the Bank : ....

Accommodation required?   YES/NO

DECLARATION:

I declare that all the details furnished above are true to the best of my knowledge and I agree to abide by the rules and regulations of the programme.

Date:

Place:

Signature of the Applicant:

Forwarded/Recommended:

Signature with seal of Head of the Institution/Organization

ADDRESS FOR CORRESPONDENCE

The Head of the Department,
Department of ECE,
St. Joseph's Institute of Technology,
Old Mamallapuram Road,
Chennai – 600119, Tamil Nadu.

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Organized by

**Department of
Electronics & Communication Engineering
(NBA ACCREDITED)**

St. Joseph's Institute of Technology

**St. Joseph's Group of Institutions
OMR, Chennai-119.**

in association with

ABOUT THE PROGRAMME

Machine Learning (ML) is a technique which analyses its observations and learns from its experience to make a better decision for the future. More effort is building up to perform inference on edge devices in embedded systems, to overcome network bandwidth bottleneck , security and privacy issues for which ML becomes a solution.

Thus with all the above merits of integrating ML with embedded systems, this programme is been organized to provide a overview of this integration and detailed study about the basics of embedded systems and machine learning algorithms. The Programme would detail the communication protocols for embedded systems, hardware & software development boards, session on python programming, basics of machine learning and its algorithms and also a introductory session to 3D printing. As a highlight to these sessions, one day industrial visit to Retech solutions is been planned where the 3D printing and other projects regarding the topic will be given as live demo and hands on if needed.

On the whole the FDP would surely be a platform for faculties, researchers and students for their knowledge development and interaction to pursue their research in this field.

ABOUT RETECH SOLUTIONS

RETECH Solutions Pvt. Ltd. is an innovation- focused establishment, they emphasize in providing quality products that are also unique. Their aim is to make better use of science so that the technologies will come up with is better than the best. They mainly focus on product development and also provide services like Internships, Implant training, workshops and Project assistance for upcoming Engineers. As an ISO 9001:2008 certified organisation customer satisfaction is their main goal and they ensure that their products have an international standard.

COURSE CONTENT

The following topics will be covered in FDP with hands on training

- Introduction to Embedded systems and its application
- Communication Protocols in Embedded systems.
- Detail about various development boards - Arduino Development Board
- RASPBERRY PI Development Board
- NODE MCU Development Board – STM32 Series
- Briefing about 3D –printing and projects using the boards.
- Need for ML in embedded systems.
- Introduction of Machine Learning Techniques.
- Introduction and logical analysis in python.
- Machine learning libraries in Python.
- Hands –On in ML and python.
- Industrial visit – Demo of 3D-Printing and projects.

Eligibility: Faculty from various branches of Engineering Colleges, Polytechnics, Research scholars and PG student

ORGANIZING COMMITTEE

PATRONS

Dr. B. BABU MANOHARAN,
M.A., M.B.A., Ph.D.,
Chairman, St. Joseph's Group of Institutions

Mrs.B.JESSIE PRIYA, M.com.
Managing Director,
St. Joseph's Group of Institutions

Mr. B. SHASHI SEKAR, M.Sc.
Director, St. Joseph's Group of Institutions

Dr. P. RAVICHANDRAN, M.Tech., Ph.D.,
Principal, St. Joseph's Institute of Technology

CONVENERS

Dr. C.Gnana Kousalya M.E., Ph.D.,
Dr.G.Rohini M.E., Ph.D.,

CO-ORDINATORS

Mrs. S.Tephillah, M.E. (Ph.D)
Dr. R.Raman, M.Tech, Ph.D
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VENUE: *Optical Communication Lab*

IMPORTANT DATES
Last date for Registration : 06-04- 2019
Intimation for the selection: 08-04-2019

Registration Fees:

Demand Draft for Rs.750/ - drawn in favour of "St. Joseph's Institute of Technology" payable at Chennai from any nationalized bank